

IN THE U.S. PATENT AND TRADEMARK OFFICE

Applicant: YANG, Chih An Conf.:
Appl. No.: NEW Group:
Filed: April 7, 2003 Examiner:
For: NOISE ELIMINATING SYSTEM ON CHIP AND
METHOD OF MAKING SAME

L E T T E R

Assistant Commissioner for Patents
Washington, DC 20231

April 7, 2003

COPY

Sir:

Under the provisions of 35 U.S.C. § 119 and 37 C.F.R. § 1.55(a), the applicant(s) hereby claim(s) the right of priority based on the following application(s):

<u>Country</u>	<u>Application No.</u>	<u>Filed</u>
TAIWAN, R.O.C.	091134103	November 22, 2002

A certified copy of the above-noted application(s) is(are) attached hereto.

If necessary, the Commissioner is hereby authorized in this, concurrent, and future replies, to charge payment or credit any overpayment to Deposit Account No. 02-2448 for any additional fee required under 37 C.F.R. §§ 1.16 or 1.17; particularly, extension of time fees.

Respectfully submitted,

BIRCH, STEWART, KOLASCH & BIRCH, LLP

By _____
Joe McKinney Muncy, #32,334

KM/tmr
2019-0189P

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Attachment